Product Description: T315XW02 TFT-LCD PANEL with RoHS guarantee			
AUO Model Name: T315XW02 VL			
Customer Part No/Project Name:			
Customer Signature	**Date**	AUO	**Date**
		Approved By: PM Director/ Frank Hsu	
		Reviewed By: RD Director/ Hong Hong Jye Reviewed By: Project Leader/ Kenney Chen	
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Document Version : 1.0
Date : 2008/03/07

Product Specifications

31.5” WXGA Color TFT-LCD Module
Model Name: T315XW02 VL

( ) Preliminary Specifications
(*) Final Specifications

Contents

Record of Revision

Version	Date	No	Old Description	New Description	Remark
0.1	2007/10/22		First issue		
0.2	2007/10/24	17	Contract Ratio = 1500	Contract Ratio = 2000	Updated
0.3	2007/11/15	11	D_EPWM = 20% (min)	D_EPWM = 10% (min)	Updated
0.4	2007/11/26	11	Power consumption = 92W, Input current =3.8A	Power consumption = 105W, Input current =4.38A	Updated
		7	PDDB = 92W (typ)	PDDB = 105W (typ)	Updated
05	2008/01/09	8	Item 8 & 9 include boost function related data	Remove boost function description (not supported)	Updated
		9	Connector on Panel: FI-X30SSL-HF (Manufactured by JAE) or Equivalent	093G30-B0001A-1 (Manufactured by Starconn)	Updated
		11	CN1: JST PHR-14 or equivalent	CN1: CI0114M1HR0-LF (Manufactured by Civilux) Matting: CI0114S0000 (Manufactured by Civilux) or equivalent	Updated
		11	PDIM/VDIM Duty = 20% ~ 100%	PDIM/VDIM Duty = 10% ~ 100%	Updated
		12	Horizontal Section Periold (Th) Typ = 1560	Horizontal Section Periold (Th) Typ = 1648	Updated
		18	Contrast Ratio: typ = 2000	Contrast Ratio: typ = 2500	Updated
		18		Remove Ton/Toff Response Time	Updated
		22	Vibration Test (non-operation) Wave form: random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration: X, Y, Z 20min,	Vibration Test (non-operation) (10 ~ 300Hz/1.5G/11min SR, XYZ 30min/axis) Vibration level : 1.5G RMS, Bandwidth : 10-300Hz Duration: X, Y, Z 30min,	Updated
			Vibration test (with carton) Random wave Vibration:10~200Hz,1.5Grms,30minutes Direction: ±X, ±Y, ±Z, one time each direction	Vibration test (with carton), Random wave (1.5Grms 5~500Hz) 30mins / Per each X.Y.Z axes	Updated
06		7	Power Supply Input Current typ =0.55 A	Power Supply Input Current typ =0.45 A	Updated
			Power consumption typ = 6.6 W, max = 7.5W	Power consumption typ = 5.4 W max = 7.26W	Updated
		12	Horizontal Section Periold (Th) Min = 1440,	Horizontal Section Periold (Th) Min = 1414,	Updated
			Horizontal Section Blanking (Tblk) Min = 74, Typ = 194	Horizontal Section Blanking (Tblk) Min = 48, Typ = 282	Updated
07	2008/01/30	14	Horizontal Section Periold (Th) Max = 1900, Horizontal Section Blanking (Tblk), Max = 534	Horizontal Section Periold (Th) Max = 1700, Horizontal Section Blanking (Tblk), Max = 334	Updated
		9		Add Suggestion Matting: 093E30-000220-G4 (made by Starconn or FI-X30HL (Maded by JAE) or equivalent	Updated
		12		Add input voltage, current, power max value at turn on stage	Updated

		11	Inrush current max value = 5.0 A	Inrush current max value = 6.0 A	Updated
0.8	2008/02/21	16	Falling timing range of tt6 = 10%~ 90%	Falling timing range of t6 = 20%~ 80%	Updated
0.9	2008/02/26	14	Horizontal Section Periold (Th) Max = 1700, Horizontal Section Blanking (Tblk), Max = 334	Horizontal Section Periold (Th) Max = 1706, Horizontal Section Blanking (Tblk), Max = 340	Updated
1.0	2008/03/07	22	Gap between bezel and cell = 0.7 mm	Gap between bezel and cell = 0.76 mm	Updated

1. General Description

This specification applies to the 31.51 inch Color TFT-LCD Module T315XW02 VL. This LCD module has a TFT active matrix type liquid crystal panel 1366x768 pixels, and diagonal size of 31.51 inch. This module supports 1366x768 XGA-WIDE mode (Non-interlace).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T315XW02 VL has been designed to apply the 8-bit 1 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

The T315XW02 VL model is RoHS verified which can be distinguished on panel label.

* General Information

Items	Specification	Unit	Note
Active Screen Size	**31.51 inches**		
Display Area	**697.685 (H) x 392.256(V)**	**mm**	
Outline Dimension	**760.0(H) x 450.0(V) x 45(D)**	**mm**	**With inverter**
Driver Element	**a-Si TFT active matrix**		
Display Colors	**16.7M**	**Colors**	
Number of Pixels	**1366 x 768**	**Pixel**	
Pixel Pitch	**0.51075**	**mm**	
Pixel Arrangement	**RGB vertical stripe**		
Display Mode	**Normally Black**		
Surface Treatment	**Anti-Glare, Haze =11%, 3H**		

2. Absolute Maximum Ratings

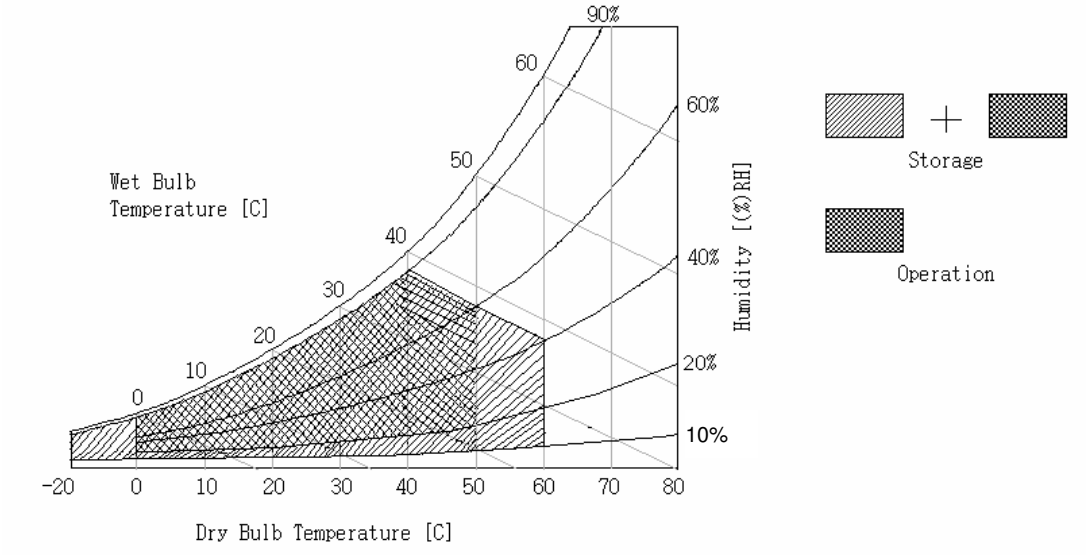
The following are maximum values which, if exceeded, may cause permanent damage to the unit.

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	3.6	[Volt]	Note 1
BLU Input Voltage	VDDB	-0.3	28	[Volt]	Note 1
BLU Brightness Control Voltage	Vdim	-0.3	7.0	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39℃ and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.



3. Electrical Specification

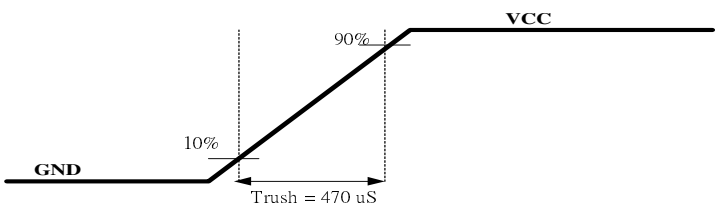
The T315XW02 VL requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the BLU, is to power inverter..

3-1 Electrical Characteristics

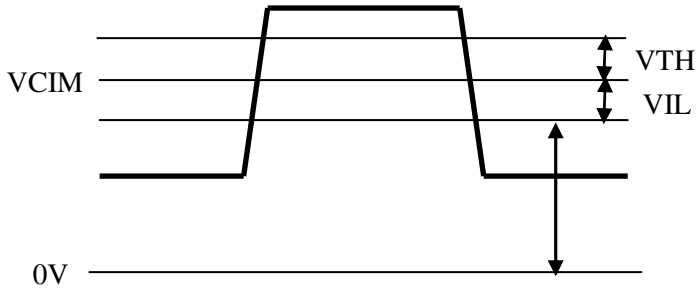
Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
LCD:							
Power Supply Input Voltage		Vcc	10.8	12	13.2	Vdc	1
Power Supply Input Current		Icc	-	0.45	0.55	A	2
Power Consumption		Pc	-	5.4	7.26	Watt	2
Inrush Current		I_{RUSH}	-	-	6	Apeak	3
LVDS Interface	Differential Input High Threshold Voltage	VTH	100			mV	4
	Differential Input Low Threshold Voltage	VTL	100			mV	4
	Common Input Voltage	VCIM	1.10	1.25	1.40	V	4
CMOS Interface	Input High Threshold Voltage	VIH (High)	2.4		3.3	Vdc	
	Input Low Threshold Voltage	VIL (Low)	0		0.7	Vdc	
Backlight Power Consumption		PDDB	99	105	111	Watt	
Life Time			50,000			Hours	5,6,7

Note :

1. The ripple voltage should be controlled under 10% of V_{CC}
2. Vcc=12.0V, f_v =60Hz, fCLK=81.5Mhz , 25℃, Test Pattern : White Pattern
3. Measurement condition :



4. VCIM = 1.2V



5. The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC Inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

6. Do not attach a conducting tape to lamp connecting wire. If the lamp wire attach to conducting tape, TFT-LCD Module have a low luminance and the inverter has abnormal action because leakage current occurs between lamp wire and conducting tape.

7. The relative humidity must not exceed 80% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃. When operate at low temperatures, the brightness of CCFL will drop and the life time of CCFL will be reduced.

3-2 Interface Connections

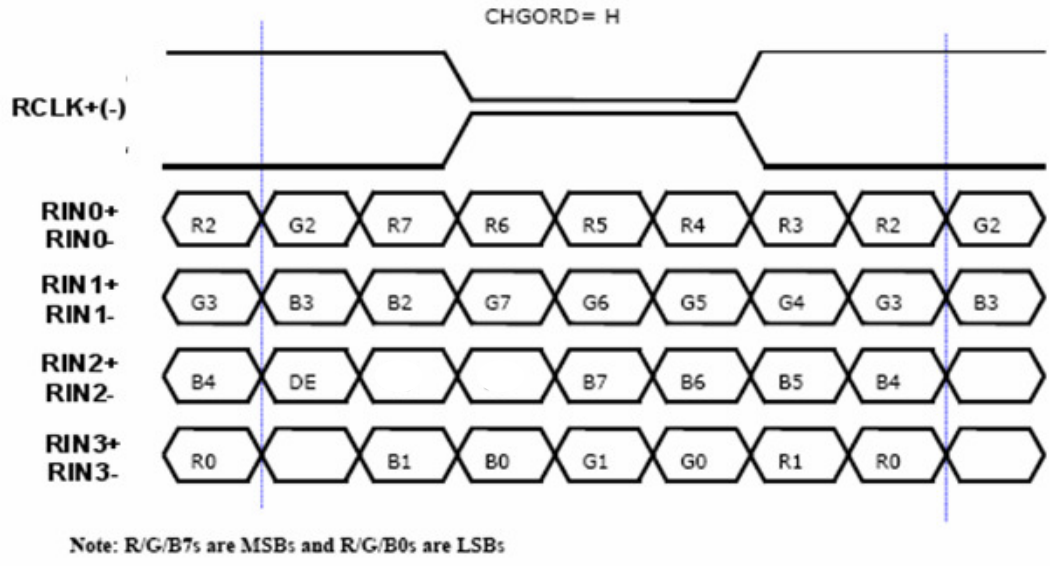
- Connector on Panel: **093G30-B0001A-1** (Manufactured by Starconn)
- Suggestion Matting: 093E30-000220-G4 (made by Starconn) or FI-X30HL (Maded by JAE) or equivalent

Pin No	Symbol	Description	Default
1	VCC	+12V, DC, Regulated	
2	VCC	+12V, DC, Regulated	
3	VCC	+12V, DC, Regulated	
4	VCC	+12V, DC, Regulated	
5	GND	Ground and Signal Return	
6	GND	Ground and Signal Return	
7	GND	Ground and Signal Return	
8	GND	Ground and Signal Return	
9	LVDS Option	Low/Open for Normal (NS), High for JEIDA	NS mode
10	Reserved	Open	AUO internal test
11	GND	Ground and Signal Return for LVDS	
12	RIN0-	LVDS Channel 0 negative	
13	RIN0+	LVDS Channel 0 positive	
14	GND	Ground and Signal Return for LVDS	
15	RIN1-	LVDS Channel 1 negative	
16	RIN1+	LVDS Channel 1 positive	
17	GND	Ground and Signal Return for LVDS	
18	RIN2-	LVDS Channel 2 negative	
19	RIN2+	LVDS Channel 2 positive	
20	GND	Ground and Signal Return for LVDS	
21	RCLK-	LVDS Clock negative	
22	RCLK+	LVDS Clock positive	
23	GND	Ground and Signal Return for LVDS	
24	RIN3-	LVDS Channel 3 negative	
25	RIN3+	LVDS Channel 3 positive	
26	GND	Ground and Signal Return for LVDS	
27	Reserved	Open or High	AUO internal test
28	Reserved	Open or High	AUO internal test
29	GND	Ground and Signal Return	
30	GND	Ground and Signal Return	

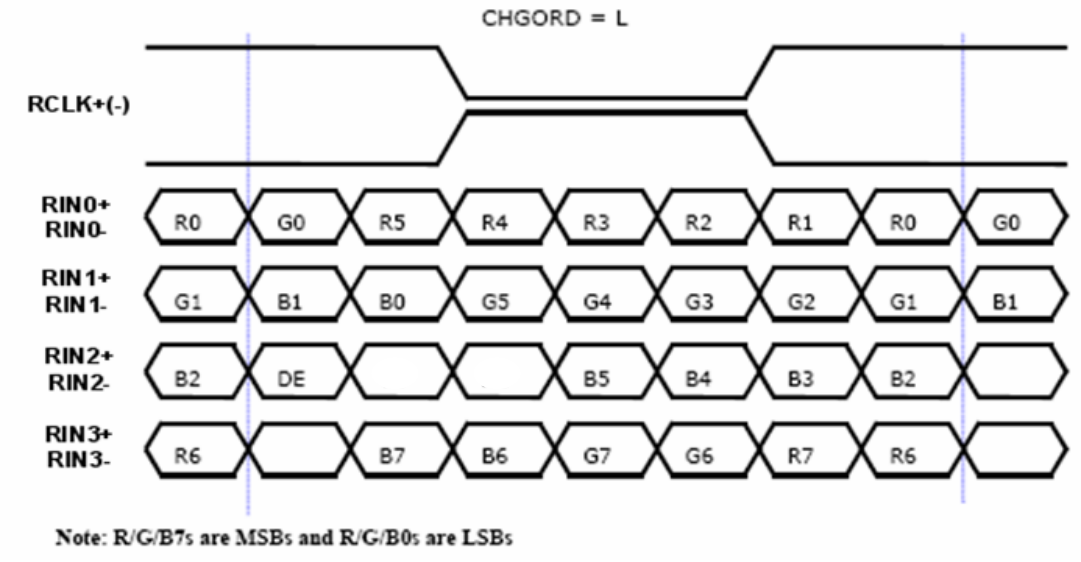
Note:

1. All GND (ground) pins should be connected together and should also be connected to the LCD's metal frame. All Vcc (power input) pins should be connected together.
2. For Pin 10, 27 and 28, panel will not damage if negligently connect these pins to high or low

LVDS Option = High



LVDS Option = Low or Open



BACKLIGHT CONNECTOR PIN CONFIGURATION -

1. Electrical specification

Item	Symb.		Condition	Spec			Unit	Note
				Min	Typ	Max		
Input Voltage	VDDB		---	21.6	24.0	26.4	VDC	
Input Current (Turn on Condition)	IDDB		VDDB=24V	--	--	5.1	ADC	
Input Power (Turn on Condition)	PDDB		VDDB=24V	--	115.0	122.0	W	
Input Current (Stable on Condition)	IDDB		VDDB=24V	4.13	4.38	4.63	ADC	
Input Power (Stable on Condition)	PDDB		VDDB=24V	99.0	105.0	111.0	W	
Inrush Current	IRUSH		VDDB=24V	---	---	6.0	ADC	1
On/Off Control Voltage	VBLON	ON	VDDB=24V	2.0	---	5.0	VDC	
		OFF	VDDB=24V	0.0	---	0.8		
On/Off Control Current	IBLON		VDDB=24V	0.0	---	1.5	mADC	
Dimming Control Voltage	VDIM	MAX	VDDB=24V	---	3.3	---	VDC	2
		MIN	VDDB=24V	---	0.0	---		2
PWM Function	V_PWM	MAX	---	2.0	---	5.0	VDC	
		MIN	---	0.0	---	0.8		
External PWM Control Current	I_EPWM		---	---	---	1.5	mADC	
External PWM Duty Ratio	D_EPWM		---	10	---	100	%	3
External PWM Frequency	F_EPWM		---	120	180	300	Hz	

Note 1 : Measurement condition Rising time = 20 ms (VDDB : 10%~90%);

Note 2 : VDIM= 3.3V (MAX, 100% brightness), VDIM= 0V (MIN, 10% brightness) （Ta=25±5℃, Turn on for 45minutes）

Note 3 : (a) Uniformity and flicker does not guarantee under 10% dimming control.
(b) 10% dimming function okay and no backlight shut down

2. Input specification

CN1: CI0114M1HR0-LF (Manufactured by Civilux)

Matting: CI0114S0000 (Manufactured by Civilux) or equivalent

No	Symbol	Description
1	VDDB (Main Power)	DC input 24.0 VDC
2	VDDB (Main Power)	DC input 24.0 VDC
3	VDDB (Main Power)	DC input 24.0 VDC
4	VDDB (Main Power)	DC input 24.0 VDC
5	VDDB (Main Power)	DC input 24.0 VDC
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
10	GND	Ground
11	DET Function	Panel Status Detect, (Normal = 0~0.8V, Abnormal = Open Connector)
12	VBLON (Enable Pin)	On/Off control Signal; High/Open: On; Low: Off
13	VDIM/PDIM (LCD Bright)	VDIM: Internal PWM Dimming control signal input (DC 0~3.3V, Duty: 10%~100%) PDIM: External PWM Dimming control signal input (AC 0~3.3V, Duty: 10%~100%) (3.3V : Maximum brightness, 0V min brightness)
14	VDIM/PDIM Selection	VDIM: High/Open, PDIM: GND

3-3 Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

* Timing Table

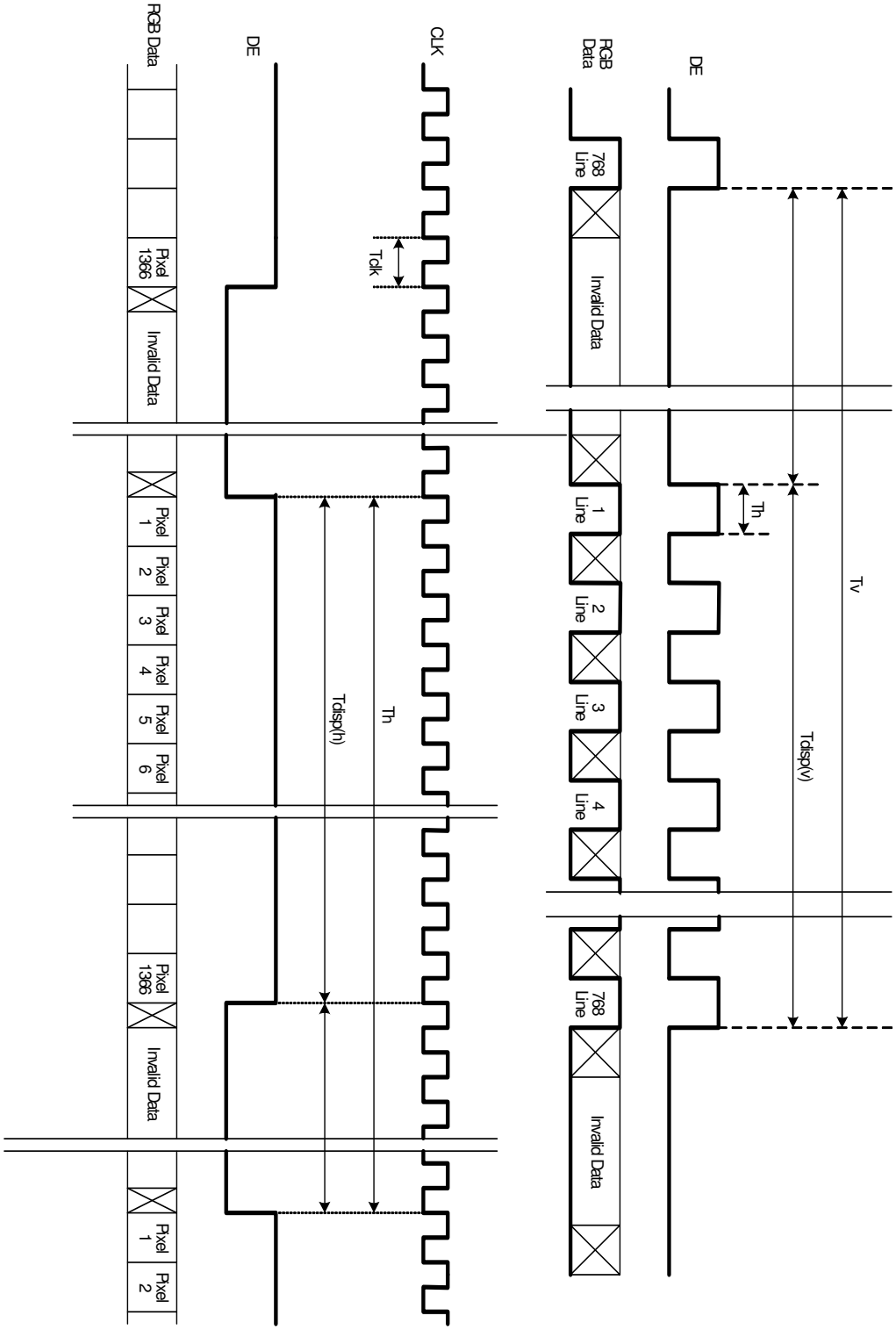
DE only Mode

Vertical Frequency:

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Notes
Vertical Section	Period	Tv	784	806	1015	Th	
	Active	Tdisp(v)		768		Th	
	Blanking	Tblk(v)	16	38	247	Th	
Horizontal Section	Period	Th	1414	1648	1706	Tclk	
	Active	Tdisp(h)		1366		Tclk	
	Blanking	Tblk(h)	48	282	340	Tclk	
LVDS Clock	Frequency	Fclk (1/Tclk)	60	80	85	MHz	
Vertical Frequency	Frequency	Fv	47	60	63	Hz	
Horizontal Frequency	Frequency	Fh	43	48	53	kHz	

Notes:
1.) Display position is specific by the rise of DE signal only.
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise the of 1st DE is displayed at the top line of screen.
3.) If a period of DE "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.
4.) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

3-4 Signal Timing Waveforms



3-5 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

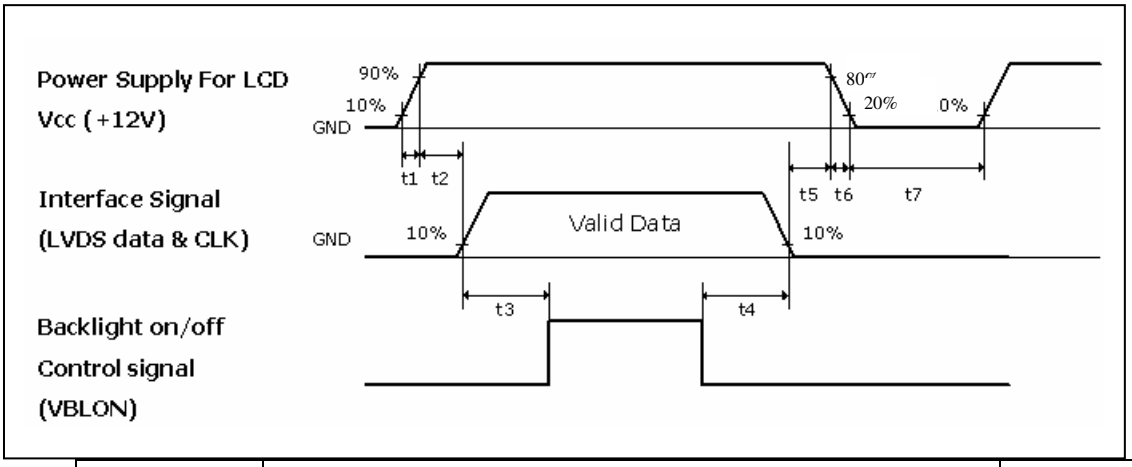
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6 Power Sequence for LCD Module

3.6.1 Power Sequence for LCD



Parameter	Values			Units
	Min.	Typ.	Max.	
t1	0.4	-	30	ms
t2	0.1	-	50	ms
t3	200	-	-	ms
t4	10	-	-	ms
t5	0.1	-	50	ms
t6		-	300	ms
t7	300	-	-	ms

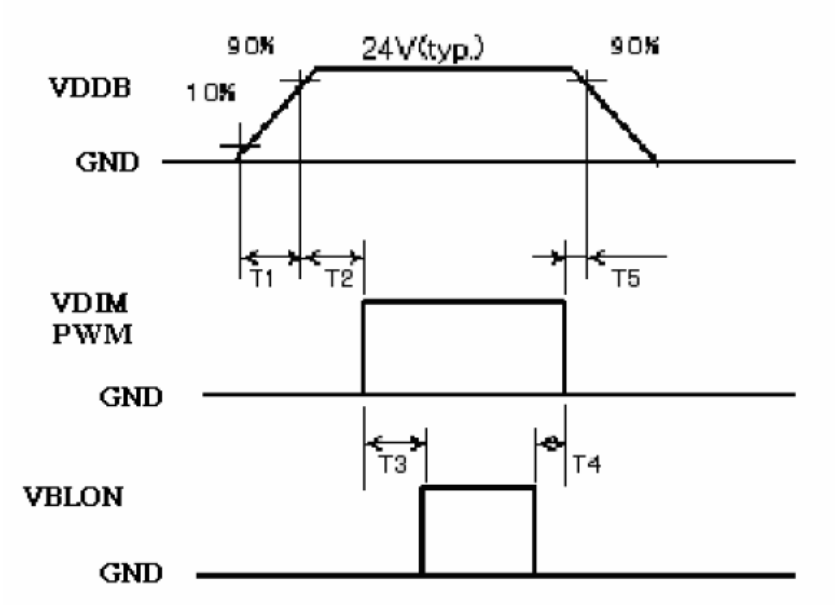
Note:

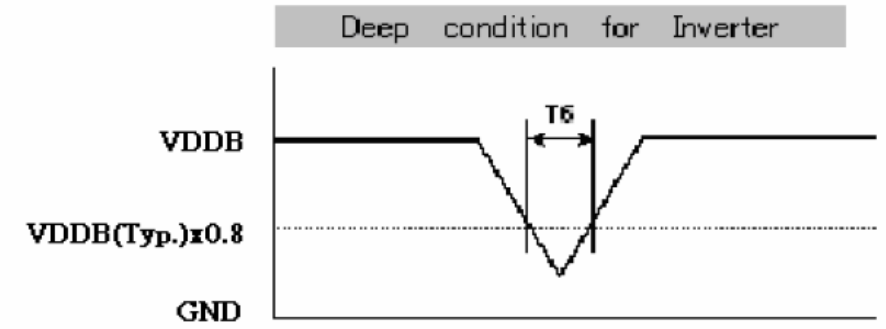
The timing controller will not be damaged in case of TV set AC input power suddenly shut down.

Once power reset, it should follow power sequence as spec. definition.

(1) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.

3.6.2 Power Sequence for Inverter





Parameter	Values			Units
	Min.	Typ.	Max.	
T1	20	-	-	ms
T2	50	-	-	ms
T3	0	-	-	ms
T4	0	-	-	ms
T5	0	-	-	ms
T6	-	-	10	ms

Note:

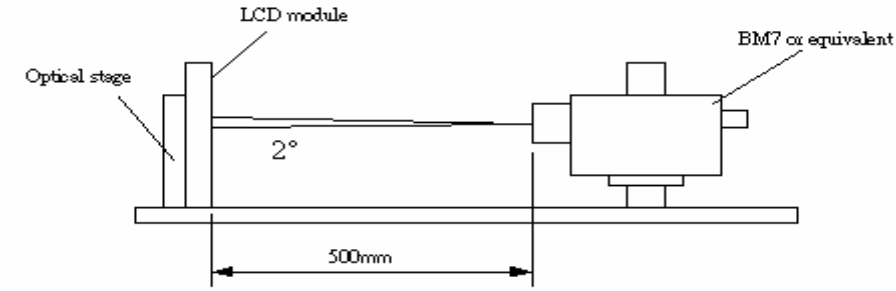
In case of t3=200ms, uniformity and optical performance are not guaranteed. But it will not damage power supplier and inverter.

In case of t7=1000ms, inverter operation is guaranteed. But brightness control depends on lamp response time.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θequal to 0°.

Fig.1 1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol	Values			Units	Notes
			Min.	Typ.	Max.		
Contrast Ratio		CR	1600	2500			1
Surface Luminance, white		LWH	400	450		cd/㎡	2
Luminance Variation		δ_{WHITE} 9 p			1.30		3
G to G Response time		Tγ		6.5	25	ms	4
Color Gamut		NTSC		72		%	
Color Coordinates							
	RED	R_X	Typ.-0.03	0.64	Typ.+0.03		
		R_Y		0.33			
	GREEN	G_X		0.28			
		G_Y		0.60			
	BLUE	B_X		0.15			
		B_Y		0.05			
	WHITE	W_X		0.28			
		W_Y		0.29			
Viewing Angle							
x axis, right(φ=0°)		θ_r		89		Degree	6
x axis, left(φ=180°)		θ_l		89			
y axis, up(φ=90°)		θ_u		89			
y axis, down (φ=0°)		θ_d		89			

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\textbf{Contrast Ratio} = \frac{\textbf{Surface Luminance of } L_{on1}}{\textbf{Surface Luminance of } L_{off1}}$$

2. Surface luminance is luminance value at point 1 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When VDDB = 24V, IDDB = 5A. L_{WH}=Lon1

   Where Lon1 is the luminance with all pixels displaying white at center 1 location.

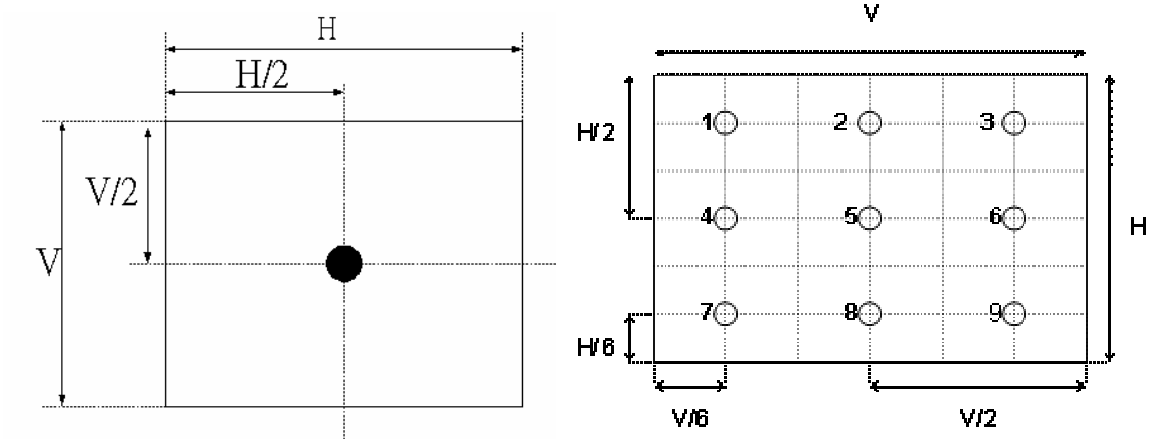
3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

   $\delta_{WHITE(9P)}$= Maximum(L_{on1}, L_{on2},…,L_{on9})/ Minimum(L_{on1}, L_{on2},…L_{on9})

4. Response time Tγ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on f_v=60Hz to optimize.

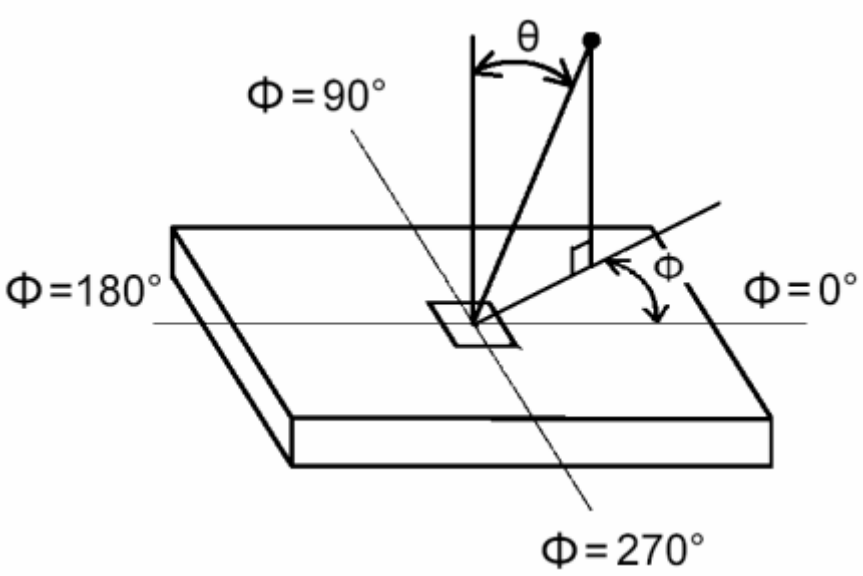
	0%	25%	50%	75%	100%
0%		t0%-25%	t0%-50%	t0%-75%	t0%-100%
25%	t25%-0%		t25%-50%	t25%-75%	t25%-100%
50%	t50%-0%	t50%-25%		t50%-75%	t50%-100%
75%	t75%-0%	t75%-25%	t75%-50%		t75%-100%
100%	t100%-0%	t100%-25%	t100%-50%	t100%-75%	

FIG. 2 Luminance



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing angle

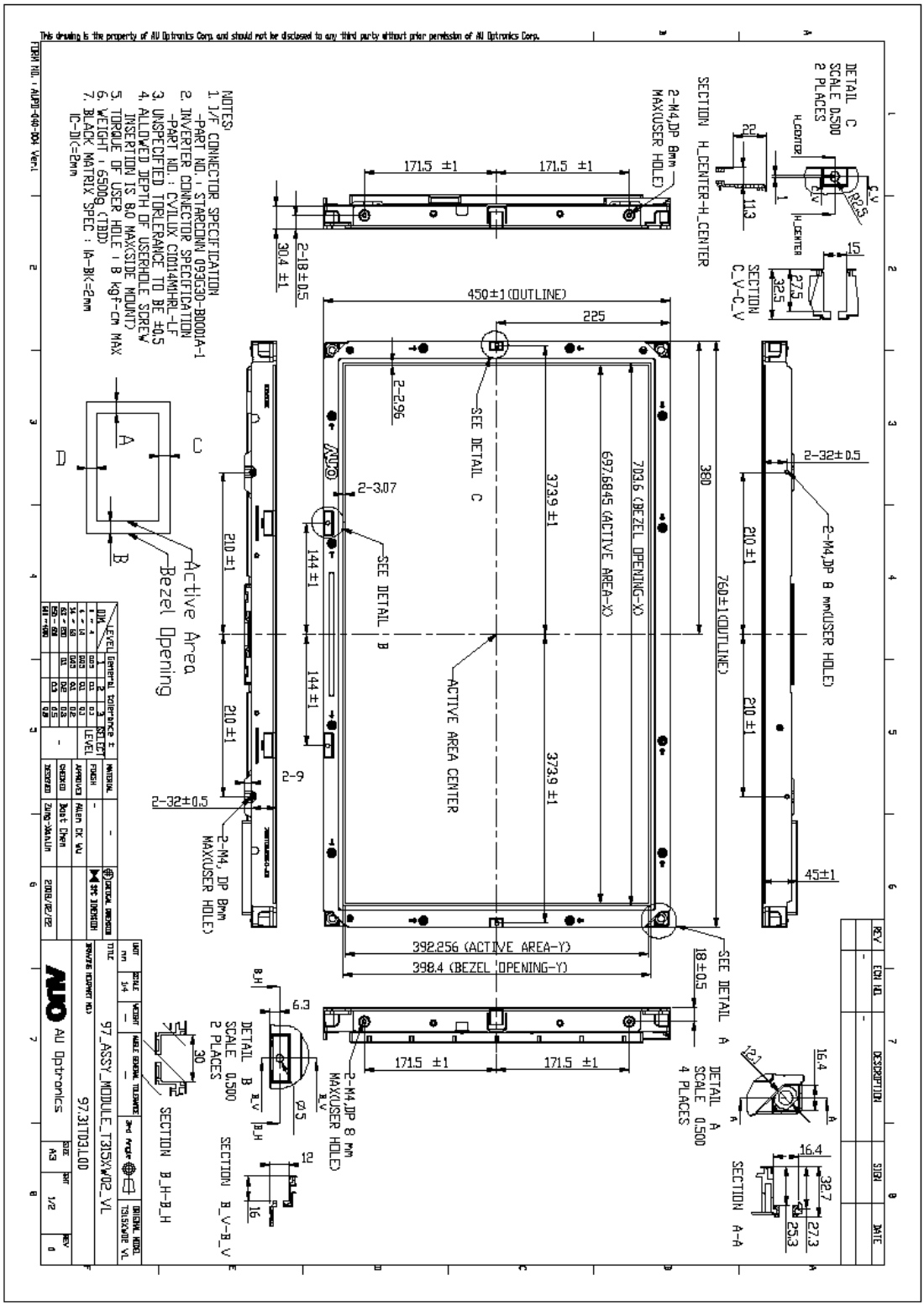


5. Mechanical Characteristics

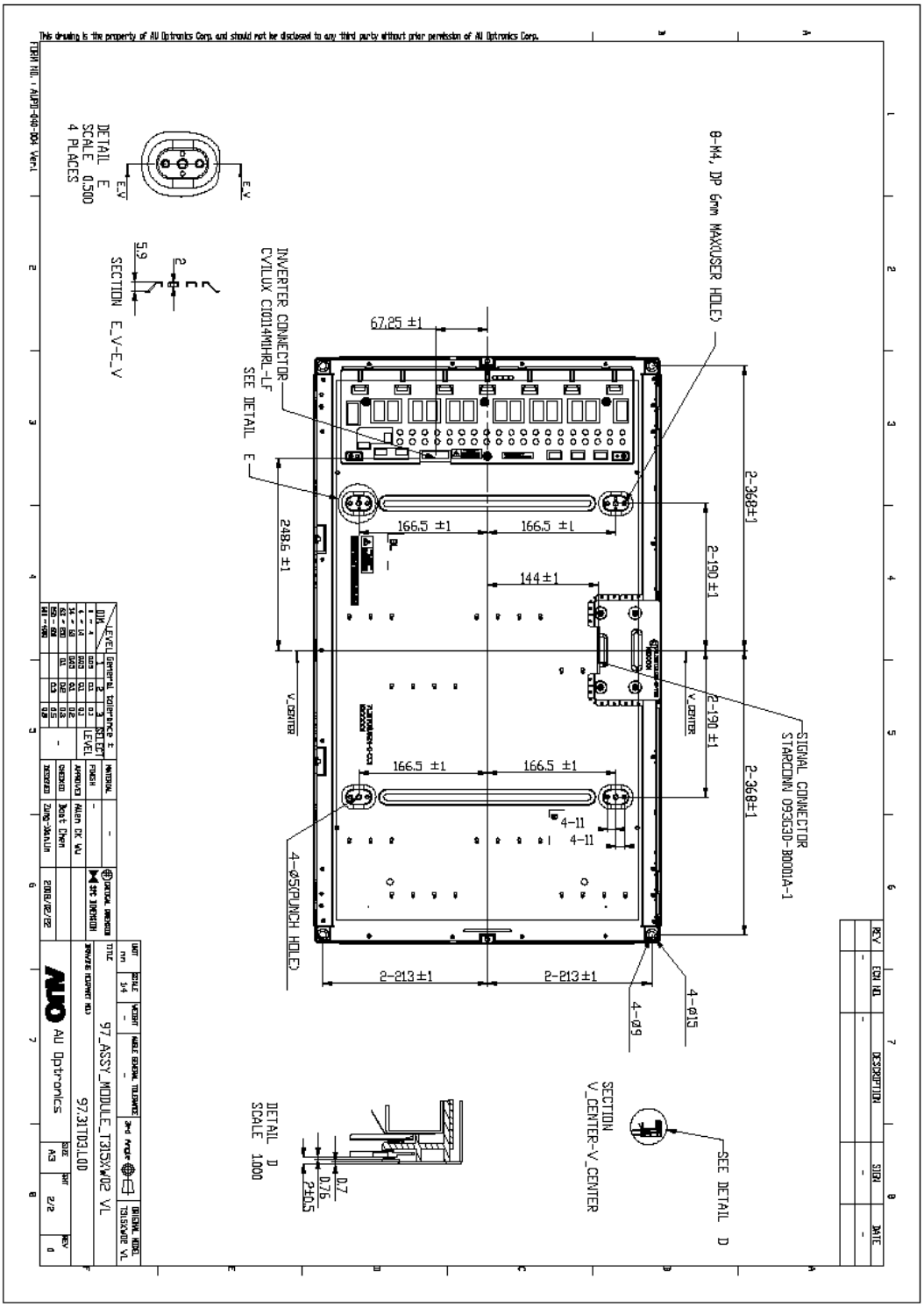
The contents provide general mechanical characteristics for the model T315XW02 VL. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	760.0mm
	Vertical	450.0mm
	Depth	45mm
Bezel Opening	Horizontal	703.6mm
	Vertical	398.3mm
Active Display Area	Horizontal	697.685mm
	Vertical	392.256mm
Weight	6500g Typ.	
Surface Treatment	AG, 3H	

Front:



Back:



6. Reliability:

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta=60℃ 300h
2	Low temperature storage test	Ta=-20℃ 300h
3	High temperature operation test	Ta=50℃ 300h
4	Low temperature operation test	Ta=-5℃ 300h
5	Vibration test (non-operating)	"(10 ~ 300Hz/1.5G/11min SR, XYZ 30min/axis) Vibration level : 1.5G RMS, Bandwidth : 10-300Hz Duration: X, Y, Z 30min, "
6	Shock test (non-operating)	Shock level: 50G Waveform: half since wave, 11ms Direction: ±X, ±Y, ±Z, one time each direction
7	Vibration test (with carton)	Random wave (1.5 Grms 5~500Hz) 30mins / Per each X.Y.Z axes "
8	Drop test (with carton)	Height: 46cm 1 corner, 3 edges, 6 surfaces (ASTMD4169-I)

7. International Standard

7-1. Safety

(1) UL1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995

Standard for Safety of Information Technology Equipment Including electrical Business Equipment.

(2) CAN/CSA C22.2 No. 950-95/60950 Third Edition, Canadian Standards Association,

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

(3) EN60950 : 1992+A2: 1993+A2: 1993+C3: 1995+A4: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+C3: 1995+A4:1996

European Committee for Electrotechnical Standardization (CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

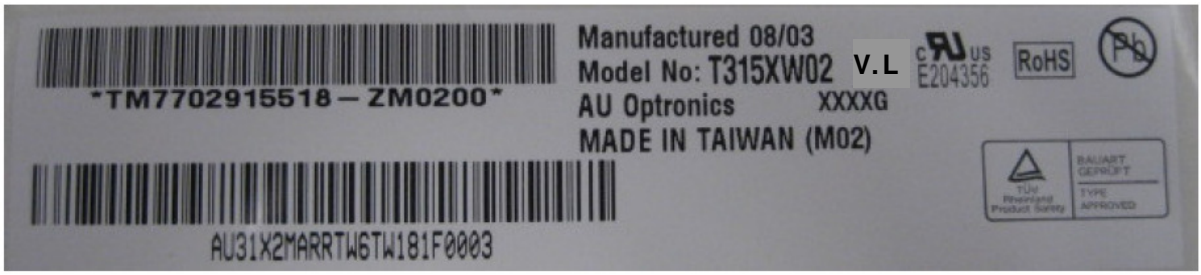
a) ANSI C63.4 “Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. “American National standards Institute(ANSI), 1992

b) C.I.S.P.R “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” International Special committee on Radio Interference.

c) EN 55022 “Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment.” European Committee for Electrotechnical Standardization. (CENELEC), 1998

8. Packing

Panel label:



TM7702915518-ZM0200

TM77029: T: Taiwan, A/B: China

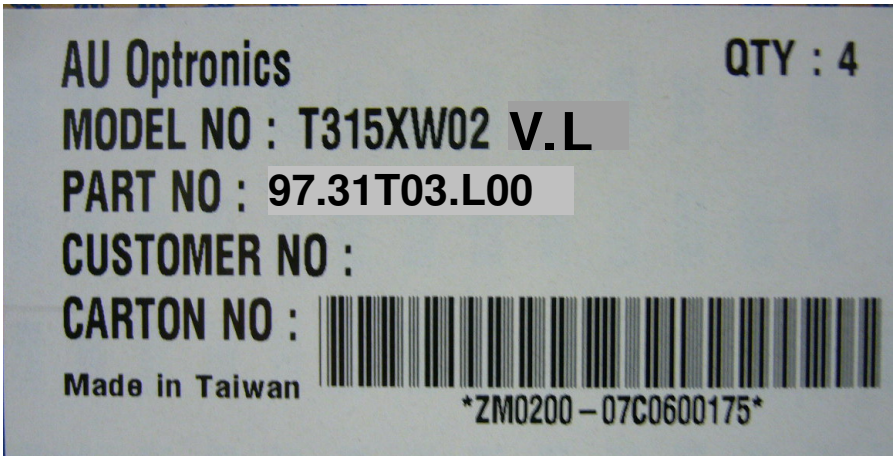
15518: Panel Serial Number

ZM0: AUO internal code

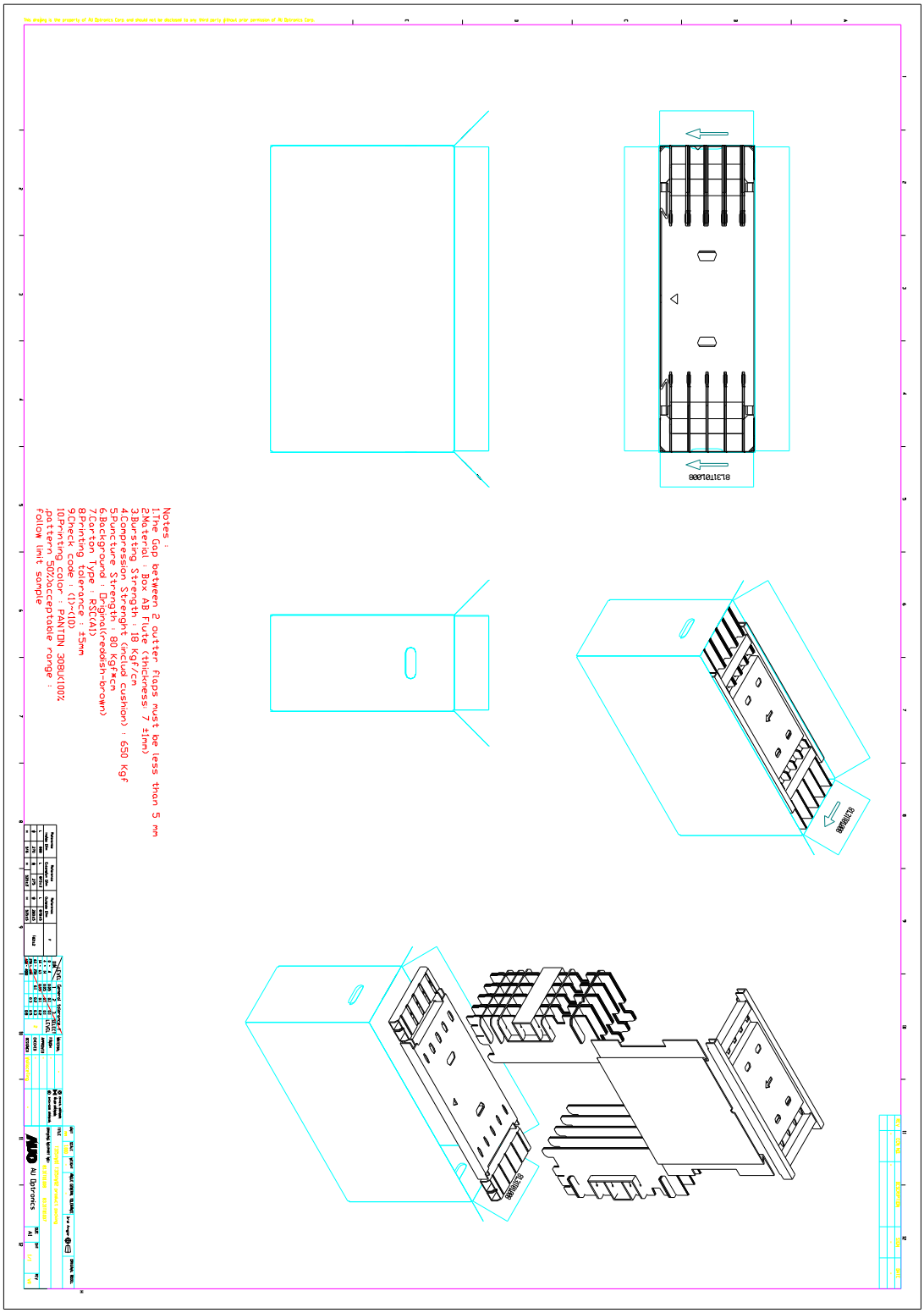
Manufactured 08/03: 2008 week 03

Made In Taiwan: Taiwan made

Carton Label:



Carton:



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1 MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2 OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:
V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature. (In lower temperature, it becomes longer..)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5 STORAGE＋

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of flue still on the Bezel after the protection film is peeled off.

(3) You can remove the glue easily. When the glue remains on the Bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.